# Electrical Characterization of CIGSSe Photovoltaic Modules

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Abstract—This work describes the electrical characterization of CIG(S,Se)2 sub-modules in order to compare the related performances with those obtained from photovoltaic traditional silicon modules. This characterization has been performed through means of a test bench, mainly composed by a solar simulator equipment, allowing the determination of the parameters of the prototypes under test at different irradiances and temperatures.

*Index Terms*-- CIGSSe modules, PV devices, electrical characterization Introduction

## I. INTRODUCTION

Nowadays, a massive use of renewable energy resources, especially for the solar energy, could be a favorable option towards a sustainable development of the human community. An important class of thin film solar cells consists of the CuInGa(S,Se)<sub>2</sub> (CIGSSe) type, composed by Copper, Indium, Gallium, Sulfur and Selenide [1-8]. This low-power PV technology could be a significant alternative towards the traditional silicon photovoltaic modules, obtaining performances almost comparable to each other [9-18]. Indeed, because of their relatively good electro-optical properties, the thin film integrated PV have achieved adequate efficiencies. Furthermore, this technology is cost-effective if compared to solar cells based on bulk absorbers. In this context, the aim of this work is to present the experimental electrical characterization of CIG(S,Se)<sub>2</sub> sub-modules in order to compare the related performances with those obtained from traditional silicon photovoltaic modules. This characterization has been performed through means of a test bench, mainly composed by a solar simulator equipment, allowing the determination of the parameters of the prototypes under test at different irradiances and temperatures. The procedures applied in the present article are the result of previous applications [19-30]. This work is composed by the following Sections: Section II describes the test bench

set-up for the PV modules characterization, Sections III reports the experimental

tests obtained for different values of irradiance, whereas the measurements at different values of temperature are discussed in Section IV.

## II. TEST BENCH DESCRIPTION

In order to electrically characterize the PV modules at different values of both irradiance and temperature, an experimental test bench has been set-up at the DEIM Department and its schematic representation is shown in fig. 1. The test bench is mainly composed by:

- •An Oriel Solar Simulator (Class AAA), equipped with an inner 450 W arc lamp, which allows to light in a uniform and stable manner a 2"x 2" square area with a spectrum that simulates the solar spectrum.
- •A 2440 Source-meter (Keithley Instruments), which allows to measure the voltage applied to the test samples and the related current.
- •A GPIB-USB data acquisition board (National Instruments), for the acquisition of the main electrical quantities involved during the tests.
- A PC integrated with the source-meter and equipped with the Oriel IV Test Station, for the real-time control and supervision of the tests.
- A reference crystalline silicon PV cell, used for the irradiance regulation.
- •A Reference cell meter, which is connected to the reference cell, in order to detect both the irradiance and the temperature of the reference cell.
- •A temperature detection system, which is used for the real-time temperature measurement of both the environment and the module under test. This system is composed by a Data Logger (Delta OHM Inc., model DO 9847), equipped with two temperature probes with Pt100 sensors.
- •An adequate cooling system, which is used to locally cool down the samples under test.

- A silicone-coated heating mat, equipped with a DC power supply (Kenwood Inc.), which is used for the electrical characterization of the samples at different values of their temperature.
- •A Nortech Reflex fiber optic thermometer, which is equipped with two fiber optic temperature probes.
- •Ten CIG(s,se)<sub>2</sub> PV modules samples, whose main characteristics are reported in Table I.

 TABLE I

 CHARACTERISTICS OF THE PV SAMPLES

Overall dimensions [cmxcm]	5.5 x 5.5
Rated current [mA]	80

A photograph of the proposed test bench is shown in fig. 2, whereas fig. 3 shows an image of the samples under test.



Fig. 1- Schematic representation of the test bench for the electrical characterization of PV cells.



Fig. 2- A photograph of the proposed test bench.



Fig. 3- A photograph of one of the ten CIG(S,Se)<sub>2</sub> PV modules samples under test.

## III. EXPERIMENTAL TESTS FOR DIFFERENT VALUES OF IRRADIANCE

For the electrical characterization of the proposed samples for different values of applied irradiance, the PV modules under test have been placed on a vacuum chuck platform and their terminals have been electrically connected to the Source Meter 2440. This fact has allowed to detect the I-V characteristic of the samples under each of the following irradiance conditions (exposure time - i.e. the time interval in which the samples have been exposed to the Solar Simulator at fixed temperature - 13 s, measuring 100 points of each I-V characteristic): 1 SUN, 0.8 SUN, 0.6 SUN, 0.4 SUN, 0.2 SUN. 0.1 SUN and dark condition. In addition, during the previously mentioned tests, the trends as function of the irradiance of the main electrical parameters as efficiency and generated power have been determined. Thus, for each of the ten samples under test, the following electrical parameters have been determined:

- *V*<sub>OC</sub> [V], open-circuit voltage;
- *I*<sub>SC</sub> [A], short-circuit current;
- $P_{\text{max}} = V_m I_m$  [mW], maximum generated power (where  $V_m$  and  $I_m$  are the voltage and current corresponding to the point of maximum power);
- $ff = V_m I_m / V_{OC} I_{SC}$  [%], fill factor;
- $R_V_{OC}$  [ $\Omega$ ], series resistance;
- $R_{I_{SC}}[\Omega]$ , shunt resistance;
- $\eta = P_{\text{max}}/P_s$  [%], efficiency (where  $P_{\text{max}}$  is the maximum electric output power of PV device and  $P_s$  is the incident solar power).

For instance, fig. 4 shows the *I*-*V* characteristics of all the ten samples under test at 1 SUN. It is possible to notice that, even though the modules are referred to the same sample lot, the values of  $V_{OC}$  and  $I_{SC}$  and the trend gradients are relatively different from each other.

By applying the same procedure above described, the



Fig. 4 - I - V characteristics of the samples under test at 1 SUN.

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Fig. 5 - I-V characteristics of the samples under test in dark condition.

*I-V* characteristics for all the imposed irradiance conditions have been determined and, as example, fig. 5 shows the *I-V* characteristics in dark condition.

Other interesting results can be observed in figures 6 and 7, where the I-V characteristics of samples nr. 3 and 4 are plotted and parametrized as function of the irradiance. From these graphs it appears evident that the values of the series resistance are relatively high for each sample under test.

Other experimental results are plotted in the graph of fig. 8, which shows the  $I_{sc}$  vs irradiance trend for the ten proposed test samples. It can be observed that  $I_{sc}$  increases almost linearly for increased valued of the applied irradiance. This is due to the increasing photoelectric effect, which is proportional to the irradiance.

Moreover, fig. 9 shows the  $V_{oc}$  vs irradiance trend for all the ten proposed test samples.  $V_{oc}$  increases for increased values of the irradiance, even though the dependence is not linear.

The overall efficiencies as function of the irradiance for samples 3 and 4 are reported in the same graph of fig. 10. It can be noticed that, in the range between 0.4 and 1 SUN, the efficiency maintains almost a constant value equal to 8.5% and 8%, respectively.



values of irradiance.



Fig. 7 – *I-V* characteristics of sample nr. 4, parametrized for different values of irradiance.



Fig. 8 - Isc vs irradiance trends for all the ten samples under test.



Fig. 9 -  $V_{oc}$  vs irradiance trends for all the ten samples under test.



## IV. EXPERIMENTAL TESTS FOR DIFFERENT VALUES OF THE SAMPLES TEMPERATURE

The samples under test have been electrically characterized also for different values of their temperature by using the same test bench described in section 1. More in particular, the samples have been tested under the following working conditions:

- Temperature range of the samples: 25° C- 70° C with steps of 5°C;
- constant value of the applied irradiance, equal at 1  $$SUN\ (1000\ W/m^2)$$
- solar spectrum equal to AM 1,5 G
- exposure time: 11 s for the measurement of 50 points of each *I-V* characteristic.

The irradiance has been regulated under the previously mentioned value by using the reference silicon cell. Furthermore, the temperature of each sample has been controlled by finely regulating the voltage applied to the heating mat. It has been experimentally observed that, during the exposure time, the temperature of the samples was increased of 1°C.



Fig. 11 - Electrical current – voltage (V-I) characteristics, related to the sample n. 3 for different values of its temperature.



Fig. 12 - *P-V* characteristics, related to the sample n. 3 for different values of the temperature.



Fig.  $13 - V_{OC}$  of sample n. 3, as function of its temperature.





Based on these assumptions, the I-V characteristics of the ten samples have been detected and the main electrical parameters reported in section 2 have been determined and parameterized for the imposed temperature range. As example, figure 11 shows the I-V characteristic of sample nr.3, parameterized for the imposed values of the surface temperature. It can be noticed that, for increased values of the temperature, a relevant decrease of  $V_{oc}$  is detected, while I<sub>sc</sub> maintains almost a constant value. The power/voltage characteristic, parameterized as function of the temperature, is plotted in fig. 12. From this graph it can be observed that, for increased values of the temperature, a significant reduction of the generated power is detected.

The trends over temperature of  $V_{oc}$ ,  $I_{sc}$  and  $\eta$  are reported in figs. 13, 14 and 15, respectively. From their analysis, it can be stated that increased values of the temperature determine a decay on its performances. As a matter of fact,  $I_{sc}$  maintains almost a constant value when the sample temperature is increased, while  $V_{oc}$  decreases with an almost linear trend. Therefore, the global efficiency decreases for increased values of temperature, as shown in fig. 15.

### V. CONCLUSIONS

In this work, the electrical characterization of CIGSSe modules has been described and discussed. An adequate test bench has been set-up and the experimental results have shown that the CIGSSe technology could be promising, but it has still to be improved.

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